Power MOSFET 32 Amps, 60 Volts, Logic Level **N–Channel DPAK**

Designed for low voltage, high speed switching applications in power supplies, converters and power motor controls and bridge circuits.

Features

- Smaller Package than MTB30N06VL
- Lower R_{DS(on)}
- Lower V_{DS(on)}
- Lower Total Gate Charge
- Lower and Tighter V_{SD}
- Lower Diode Reverse Recovery Time
- Lower Reverse Recovery Stored Charge

Typical Applications

- Power Supplies
- Converters
- Power Motor Controls
- Bridge Circuits

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V _{DSS}	60	Vdc
Drain–to–Gate Voltage (R_{GS} = 10 M Ω)	V _{DGR}	60	Vdc
Gate–to–Source Voltage – Continuous – Non–Repetitive (t _p ≤10 ms)	V _{GS} V _{GS}	±15 ±20	Vdc
Drain Current - Continuous @ $T_A = 25^{\circ}C$ - Continuous @ $T_A = 100^{\circ}C$ - Single Pulse ($t_p \le 10 \ \mu s$)	I _D I _D I _{DM}	32 22 90	Adc Apk
Total Power Dissipation @ $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$ Total Power Dissipation @ $T_A = 25^{\circ}C$ (Note 1.)Total Power Dissipation @ $T_A = 25^{\circ}C$ (Note 2.)	P _D	93.75 0.625 2.88 1.5	W W/°C W W
Operating and Storage Temperature Range	T _J , T _{stg}	–55 to +175	°C
$ Single Pulse Drain-to-Source Avalanche \\ Energy - Starting T_J = 25^\circ C (Note 3.) \\ (V_{DD} = 50 Vdc, V_{GS} = 5 Vdc, L = 1.0 mH, \\ I_{L(pk)} = 25 A, V_{DS} = 60 Vdc, R_G = 25 \Omega) $	E _{AS}	313	mJ
Thermal Resistance – Junction–to–Case – Junction–to–Ambient (Note 1.) – Junction–to–Ambient (Note 2.)	R _{θJC} R _{θJA} R _{θJA}	1.6 52 100	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	ΤL	260	°C

1. When surface mounted to an FR4 board using 1" pad size, (Cu Area 1.127 in²).

2. When surface mounted to an FR4 board using minimum recommended pad size, (Cu Area 0.412 in²).

3. Repetitive rating; pulse width limited by maximum junction temperature.

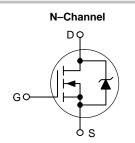


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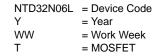
32 AMPERES 60 VOLTS

 $R_{DS(on)} = 28 m\Omega$

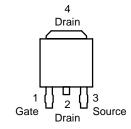


MARKING DIAGRAM





PIN ASSIGNMENT



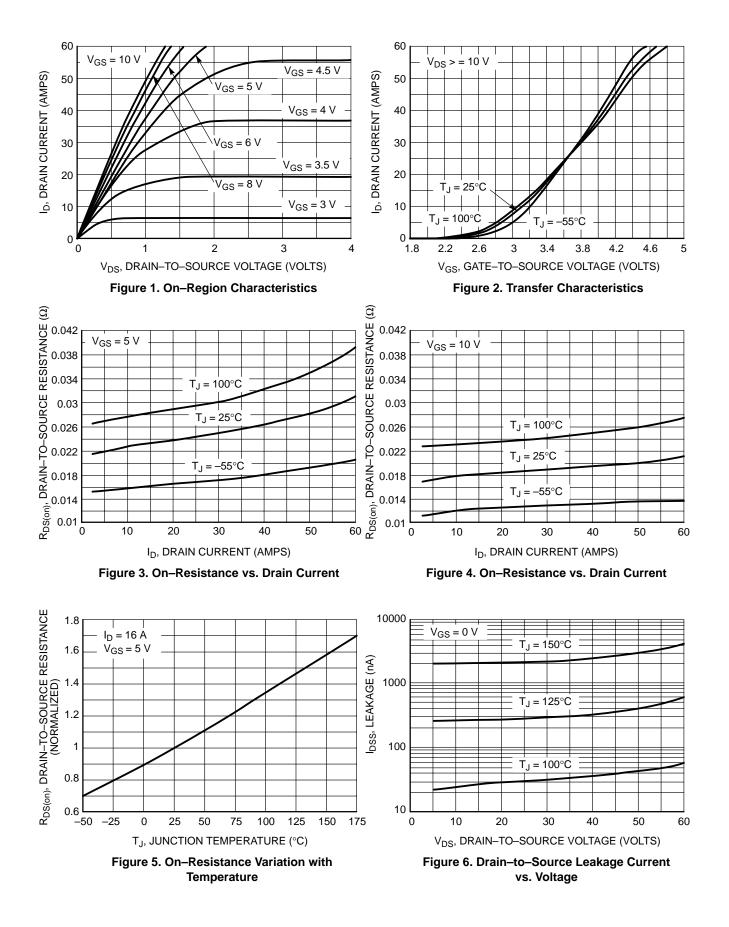
ORDERING INFORMATION

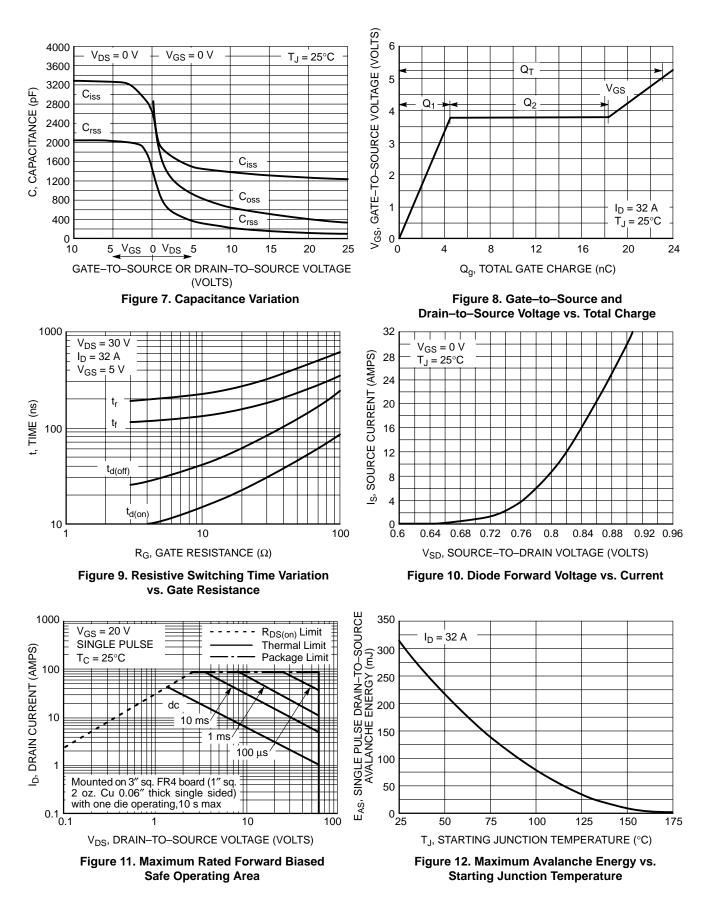
Device	Package	Shipping	
NTD32N06L	DPAK	75 Units/Rail	
NTD32N06L-1	DPAK	75 Units/Rail	
NTD32N06LT4	DPAK	2500 Tape & Reel	

ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise noted)

	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
$\begin{array}{l} \text{Drain-to-Source Breakdown} \\ (\text{V}_{\text{GS}} = 0 \text{ Vdc}, \text{ I}_{\text{D}} = 250 \ \mu\text{Ac} \\ \text{Temperature Coefficient (Pos} \end{array}$	V _{(BR)DSS}	60 -	70 62		Vdc mV/°C	
Zero Gate Voltage Drain Current ($V_{DS} = 60 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}$) ($V_{DS} = 60 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, T_J = 150^{\circ}\text{C}$)		I _{DSS}			1.0 10	μAdc
Gate-Body Leakage Current	I _{GSS}	-	-	±100	nAdc	
ON CHARACTERISTICS (Not	e 1)					
Gate Threshold Voltage (Not ($V_{DS} = V_{GS}$, $I_D = 250 \mu Adc$ Threshold Temperature Coef	V _{GS(th)}	1.0 _	1.7 4.8	2.0	Vdc mV/°C	
Static Drain-to-Source On-F ($V_{GS} = 5 \text{ Vdc}, I_D = 16 \text{ Adc}$)	R _{DS(on)}	_	23.7	28	mΩ	
$ Static Drain-to-Source On-F \\ (V_{GS} = 5 Vdc, I_D = 20 Adc) \\ (V_{GS} = 5 Vdc, I_D = 32 Adc) \\ (V_{GS} = 5 Vdc, I_D = 16 Adc, $	V _{DS(on)}	_ _ _	0.48 0.78 0.61	0.67 _ _	Vdc	
Forward Transconductance (9fs	-	27	_	mhos	
DYNAMIC CHARACTERISTIC	S	•		•	•	
Input Capacitance		C _{iss}	-	1214	1700	pF
Output Capacitance	$(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, f = 1.0 \text{ MHz})$	C _{oss}	-	343	480	
Transfer Capacitance		C _{rss}	-	87	180	
SWITCHING CHARACTERIS	TICS (Note 2)					
Turn–On Delay Time		t _{d(on)}	-	12.8	30	ns
Rise Time	$(V_{DD} = 30 \text{ Vdc}, I_D = 32 \text{ Adc},$	t _r	-	221	450	
Turn–Off Delay Time		t _{d(off)}	-	37	80	
Fall Time		t _f	-	128	260	
Gate Charge	(V _{DS} = 48 Vdc, I _D = 32 Adc, V _{GS} = 5 Vdc) (Note 1)	QT	-	23	50	nC
		Q ₁	-	4.5	-	
		Q ₂	-	14	-	
SOURCE-DRAIN DIODE CHA	RACTERISTICS					
Forward On–Voltage		V _{SD}		0.89 0.95 0.74	1.0 _ _	Vdc
Reverse Recovery Time		t _{rr}	-	56	_	ns
	(I _S = 32 Adc, V _{GS} = 0 Vdc, dI _S /dt = 100 A/µs) (Note 1)	t _a	-	31	-]
		t _b	-	25	-	
Reverse Recovery Stored Ch	Q _{RR}	-	0.093	-	μC	

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
Switching characteristics are independent of operating junction temperatures.





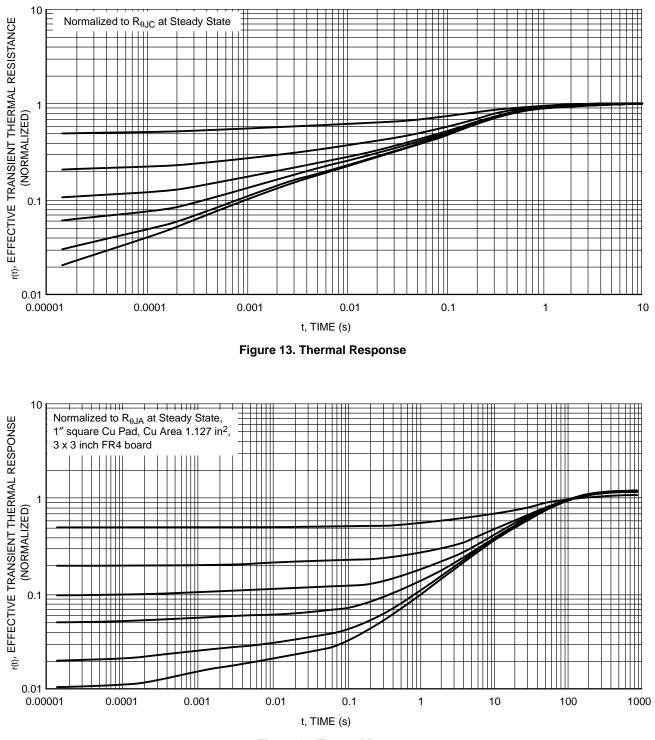
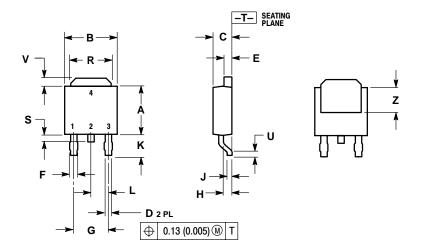


Figure 14. Thermal Response

PACKAGE DIMENSIONS

DPAK CASE 369A–13 ISSUE AB



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.235	0.250	5.97	6.35	
В	0.250	0.265	6.35	6.73	
C	0.086	0.094	2.19	2.38	
D	0.027	0.035	0.69	0.88	
Е	0.033	0.040	0.84	1.01	
F	0.037	0.047	0.94	1.19	
G	0.180 BSC		4.58 BSC		
Н	0.034	0.040	0.87	1.01	
J	0.018	0.023	0.46	0.58	
K	0.102	0.114	2.60	2.89	
L	0.090 BSC		2.29 BSC		
R	0.175	0.215	4.45	5.46	
S	0.020	0.050	0.51	1.27	
U	0.020		0.51		
۷	0.030	0.050	0.77	1.27	
Ζ	0.138		3.51		

<u>Notes</u>

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